

Project Name :A14IEXX Rev:A0

Platform : PineView -D + TigerPoint + DDR3

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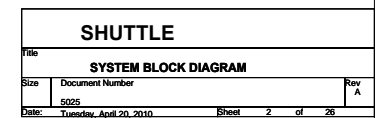
M/B Schematic Version Change List

Release Date	Version	PCB P/N	PCB Description	PCBA P/N	Note
					Main
					Main
					2ND

Daughter Board Schematic Version Change List

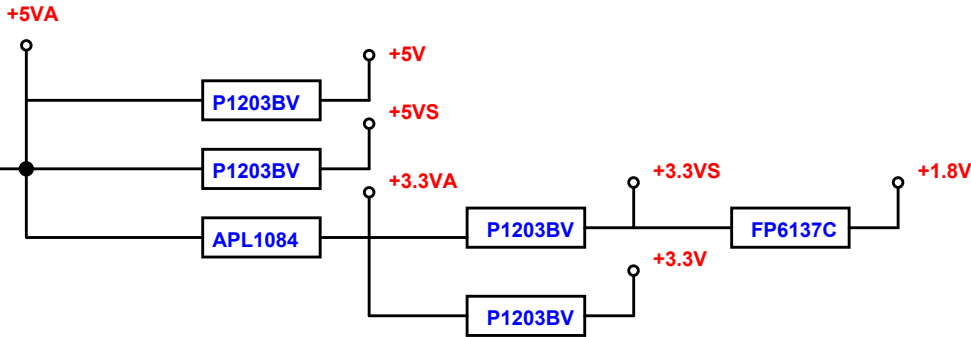
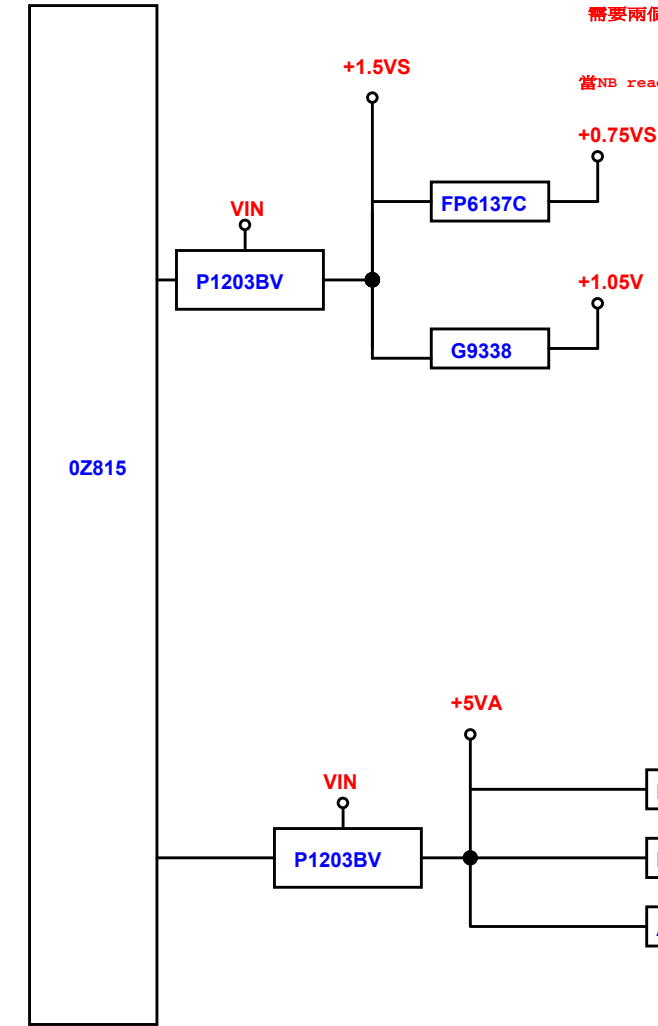
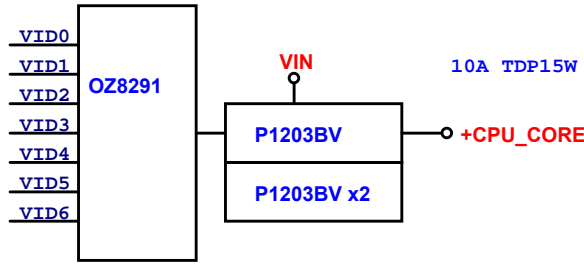
Release Date	Version	PCB P/N	PCB Description	PCBA P/N	Note

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POWER BLOCK DIAGRAM

System Poewr On Sequence



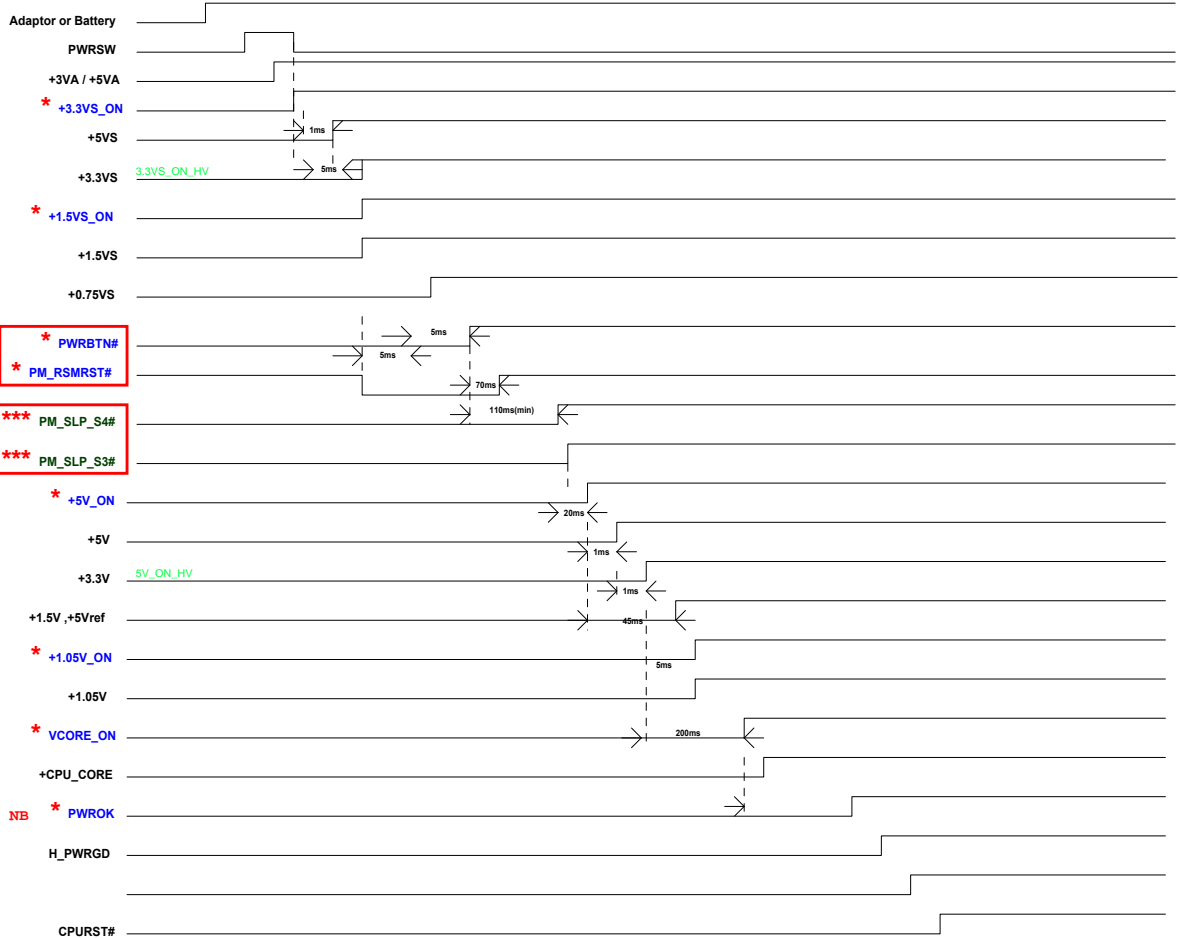
EC發給NB
需要兩個訊號收到後NB才會WORK

當NB ready 時 SUSB SUSC 為HI

EC 發給 NB

- * PWRBTN#
- * PM_RSMRST#
- *** PM_SLP_S4#
- *** PM_SLP_S3#

- * EC Control Pin (O/P)
- ** EC Control Pin (I/P)
- *** NB Control Pin (O/P)



TPT	
GPIO	
GPIO00	AUD_DETECT
GPIO01	4.7K UP TO +5V
GPIO02	PIRQE-
GPIO03	PIRQF-
GPIO04	PIRQG-
GPIO05	PIRQH-
GPIO06	NC
GPIO07	NC
GPIO08	NC
GPIO09	NC
GPIO10	EC_EXTSMI#
GPIO11	ICH_SMBALERT-
GPIO12	10K UP TO +3.3VS
GPIO13	EC_EXTSCI#
GPIO14	NC
GPIO15	10K UP TO +3.3VS
GPIO17	FLASH_STRAP2
GPIO18	
GPIO19	
GPIO20	
GPIO21	
GPIO22	4.7K UP TO +5V
GPIO23	LDRQ1#
GPIO24	GXT_ON#
GPIO25	5K DOWN TO GND
GPIO26	NC
GPIO27	NC
GPIO28	NC
GPIO29	USB_OC#4
GPIO30	USB_OC#6
GPIO31	USB_OC#6
GPIO32	
GPIO33	NC
GPIO34	NC
GPIO35	
GPIO36	4.7K UP TO +3.3V
GPIO37	
GPIO38	NC
GPIO39	NC
GPIO40	
GPIO41	
GPIO42	
GPIO43	
GPIO48	FLASH_STRAP1
GPIO49	CPUPWRGD

ITE8500E		Default
GPIO		Pull/Mode
GPA0	RF_LED_ON#	UP / GPI
GPA1	EC_BSEL0	UP / GPI
GPA2	BTL_BEEP	UP / GPI
GPA3	WLAN_PWR#	UP / GPI
GPA4	P_ID0	UP / GPI
GPA5	P_ID1	UP / GPI
GPA6	PM_RSMRST#	UP / GPI
GPA7	EC_BL_PWM	UP / GPI
GPB0	PM_SLP_S4#	UP / GPI
GPB1	PM_SLP_S3#	UP / GPI
GPB2	+1.05V_ON	Dn / GPI
GPB3	BAT_SMBCLK	/ GPI
GPB4	BAT_SMBDAT	/ GPI
GPB5	H_A20GATE	/ GPO
GPB6	H_RCIN#	UP / Func1
GPB7	PWR_USB#	Dn / GPI
GPC0	+1.5V_ON	Dn / GPI
GPC1	SMBCLK_EC	/ GPI
GPC2	SMBDAT_EC	/ GPI
GPC3	KEY_OUT16	Dn / GPI
GPC4	SAVE_PWR	Dn / GPI
GPC5	KEY_OUT17	Dn / GPI
GPC6	WLAN_ON	Dn / GPI
GPC7	EC_VID0	UP / GPI
GPD0	ADAP_IN	UP / GPI
GPD1	PWRBTN#	UP / GPI
GPD2	PLT_RST#	UP / Func1
GPD3	EC_EXTSCI#	UP / GPI
GPD4	EC_EXTSMI#	UP / GPI
GPD5	USB_LED#	UP / GPI
GPD6	+5V_ON	Dn / GPI
GPD7	SET_V	Dn / GPI
GPE0	LID#	Dn / GPI
GPE1	Fastcharge_EN	Dn / GPI
GPE2	PWROK	Dn / GPI
GPE3	Vcore_ON	Dn / GPI
GPE4	PWRSW	UP / GPI
GPE5	+1.VS_ON	Dn / GPI
GPE6	3G_PWR#	Dn / GPI
GPE7	AMP_MUTE#	UP / GPI
GPF0	EC_VID4	UP / GPI
GPF1	3G_ON	UP / GPI
GPF2	EC_VID3	UP / GPI
GPF3	CHG_ON#	UP / GPI
GPF4	TP_CLK	UP / GPI
GPF5	TP_DATA	UP / GPI
GPF6	EC_VID1	UP / GPI
GPF7	EC_VID2	UP / GPI
GPG0	100K UP TO +3.3VA	Dn/GPO/TM
GPG1	+3.3VS_ON	Dn/GPO/ID7
GPG2	FLFRAME#	
GPG6	WEBCAN_ON	
GPH0	SAFTY_PROTECT	Dn/GPI/ID0
GPH1	+1.8VS_ON	Dn/GPI/ID1
GPH2	SENBAT_V	Dn/GPI/ID2
GPH3	CHG_B_LED	Dn/GPI/ID3
GPH4	CHG_R_LED	Dn/GPI/ID4
GPH5	BATTOFF	Dn/GPI/ID5
GPH6	PWR_LED	Dn/GPI/ID6

ITE8500E		Default
GPIO		Pull/Mode
GPI0	NC	/GPI
GPI1	LCDSW0	/GPI
GPI2	LCDSW1	/GPI
GPI3	NC	/GPI
GPI4	BAT_I	/GPI/ADC
GPI5	BATT_TEMP	/GPI/ADC
GPI6	ADAPTER_I	/GPI/ADC
GPI7	BAT_V	/GPI/ADC
GPJ0	EC_BL_EN	/GPI/DAC
GPJ1	EC_PROCHOT#	/GPI/DAC
GPJ2	FAN_CTRL0	/GPI/DAC
GPJ3	CHG_REF	/GPI/DAC
GPJ4	CHG_I	/GPI/DAC
GPJ5	CRT_DETECT	/GPI/DAC

PINEVIEW-D					
	CPU	CORE (V)	ICC (A)	W	TEMP (°C)
IMVP-6+	1.2	3	3.3	90	

TPT					
	CPU	CORE (V)	ICC (A)	W	TEMP (°C)
IMVP-6+	1.2	3	3.3	90	

ITE8500E			
VCC	ICC (mA)	mW	TEMP (°C)
+3.3V	100	330	70

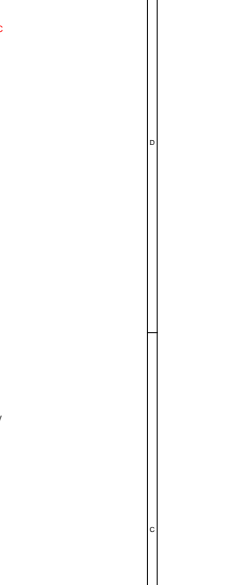
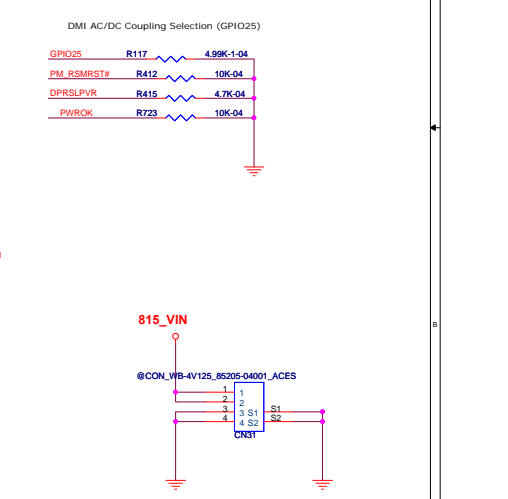
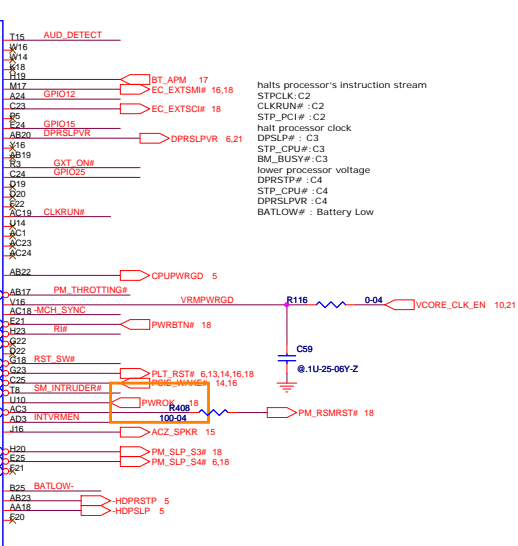
CLOCK GENERATOR			
VCC	ICC (mA)	mW	TEMP (°C)
+3.3V	250	825	70

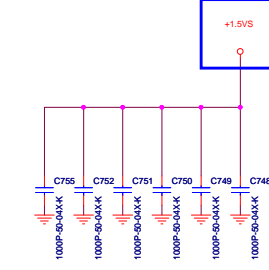
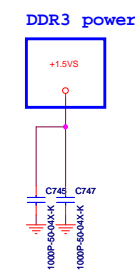
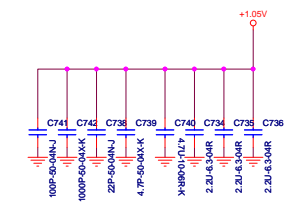
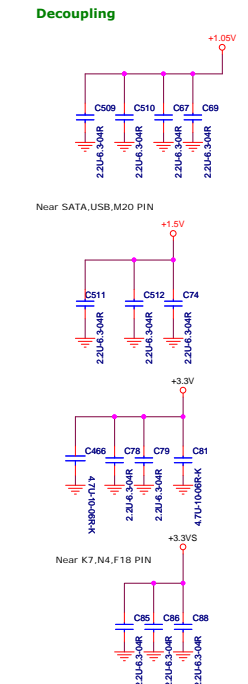
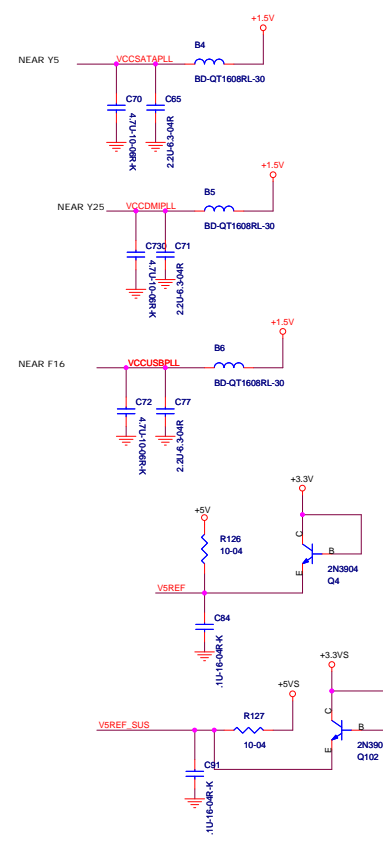
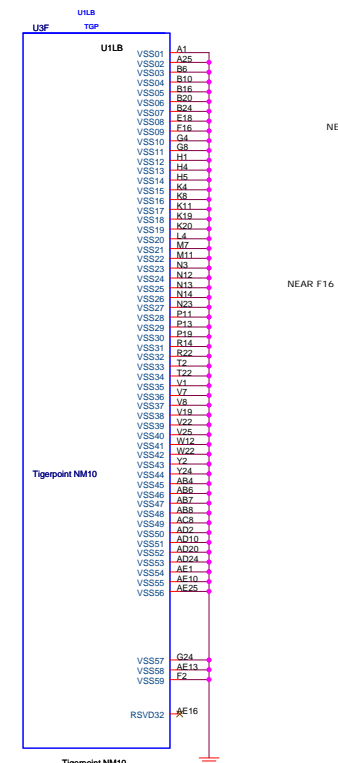
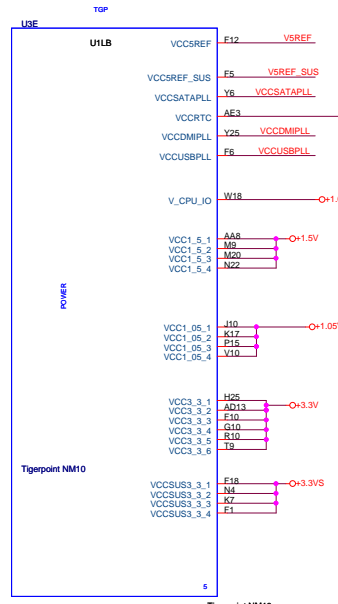
EMC1402			
VCC	ICC	mW	TEMP (°C)
+3.3V	170uA	0.56	150

CODEC			
VCC	ICC (mA)	mW	TEMP (°C)
+3.3V	250	825	70

JMC261			
VCC	ICC (mA)	mW	TEMP (°C)
+3.3V	250	825	70

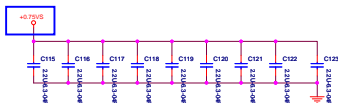
SHUTTLE			
GPIO & Power Consumption			
File	Document Number		
Size	5025	Rev	A
Date	Tuesday, April 20, 2010	Sheet	4 of 26





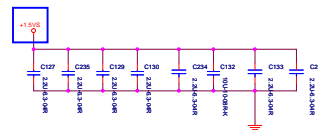
A14IEXX + DDR3			
Title			
TPT Power			
Size	Document Number	Rev	
5025		A0	
Date:	Tuesday, April 20, 2010	Sheet	9 of 26

DDR3 Power 3/11



Check bypass cap close pins

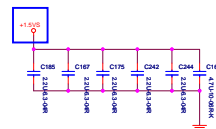
DDR3 power 3/10



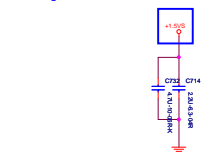
DDR3 power 3/10



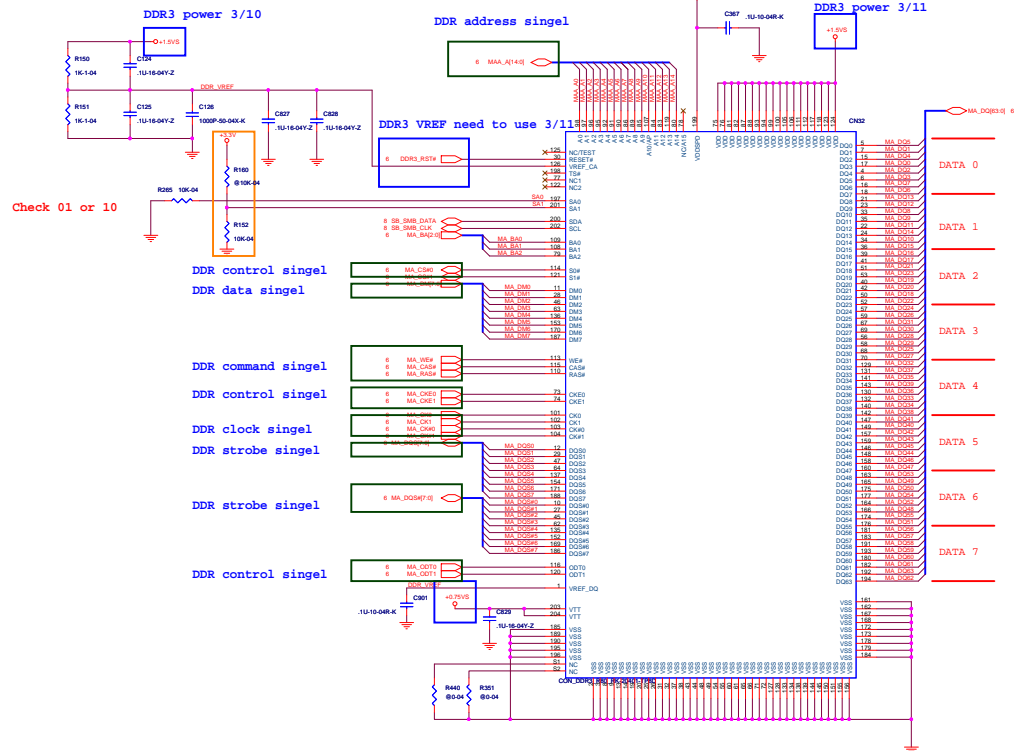
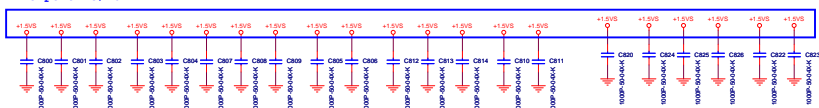
DDR3 power 3/10



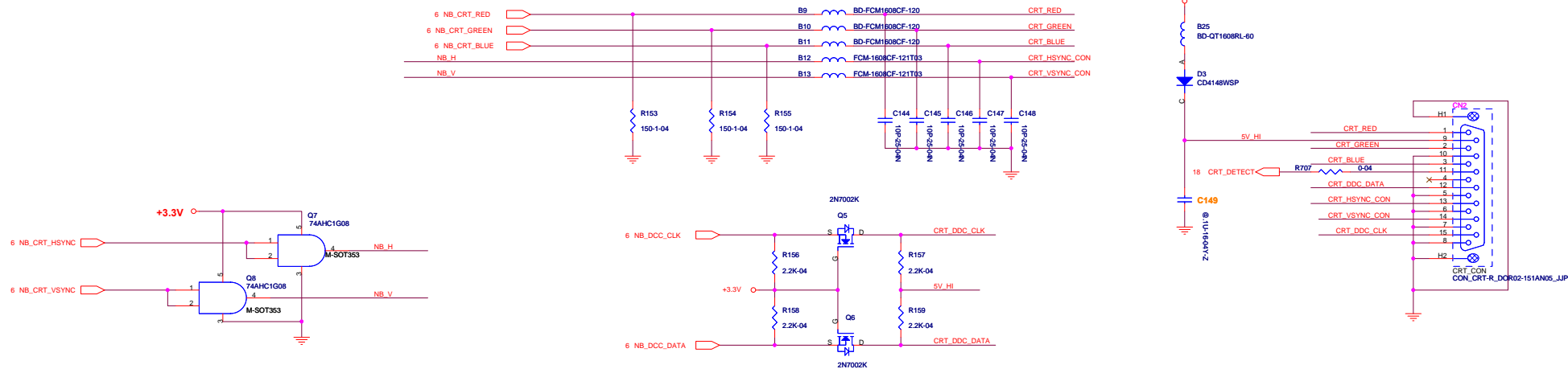
DDR3 power 3/10



DDR3 power 3/10

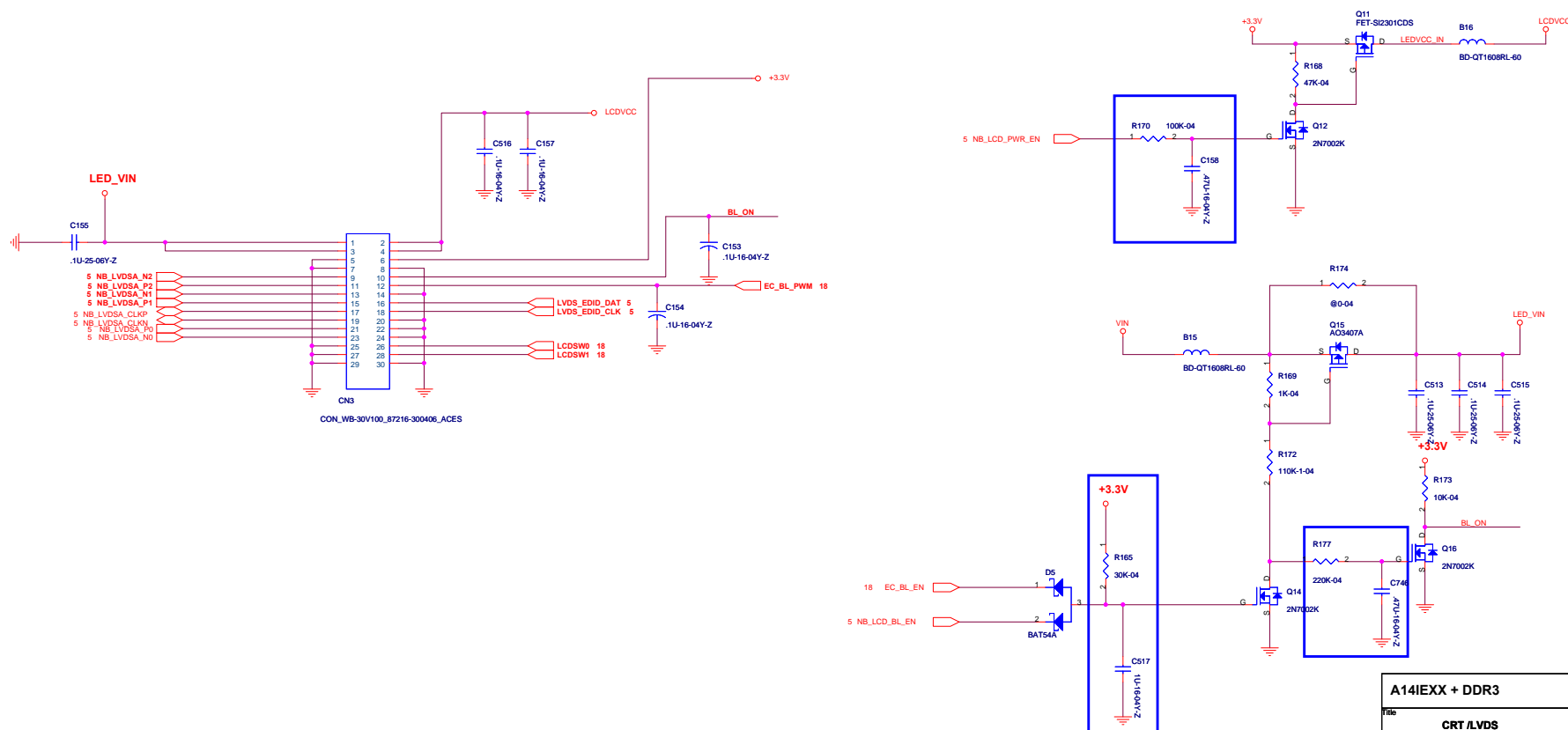


VGA Conn.



LVDS

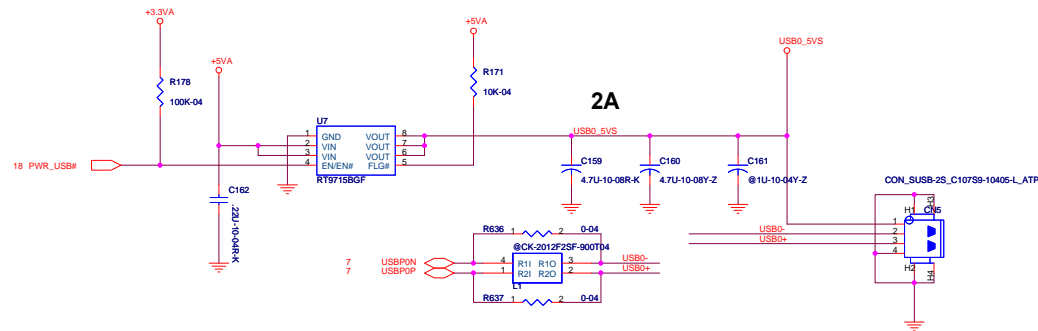
LED option



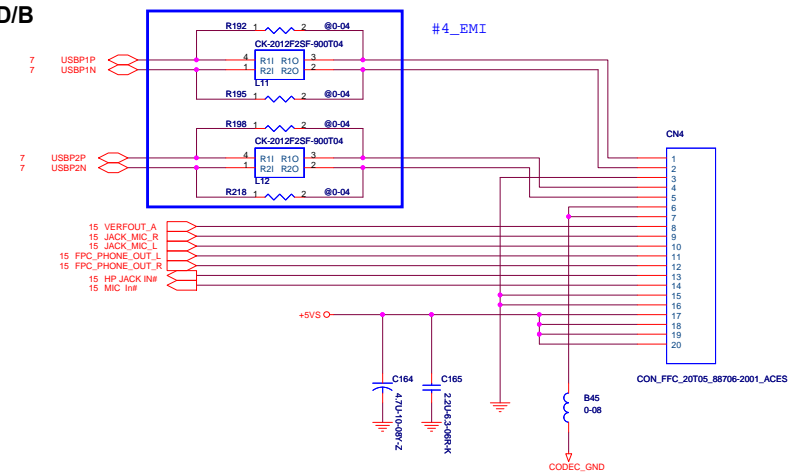
A14IEXX + DDR3

CRT /LVDS		
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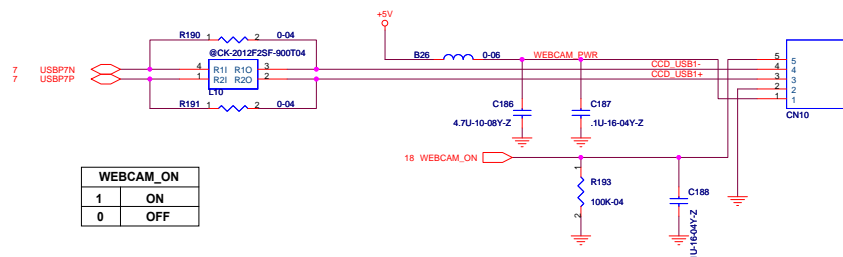
ENHANCE USB



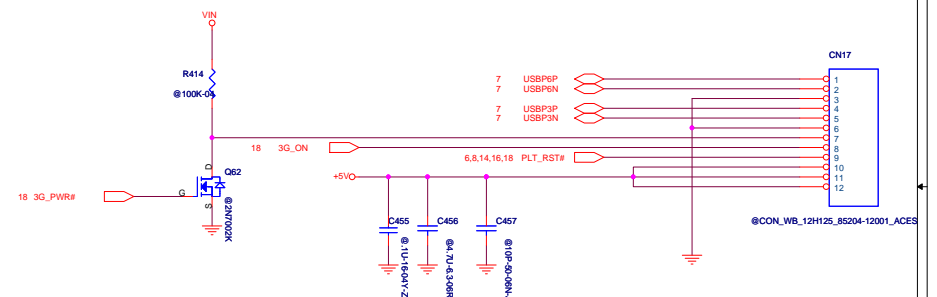
USB D/B



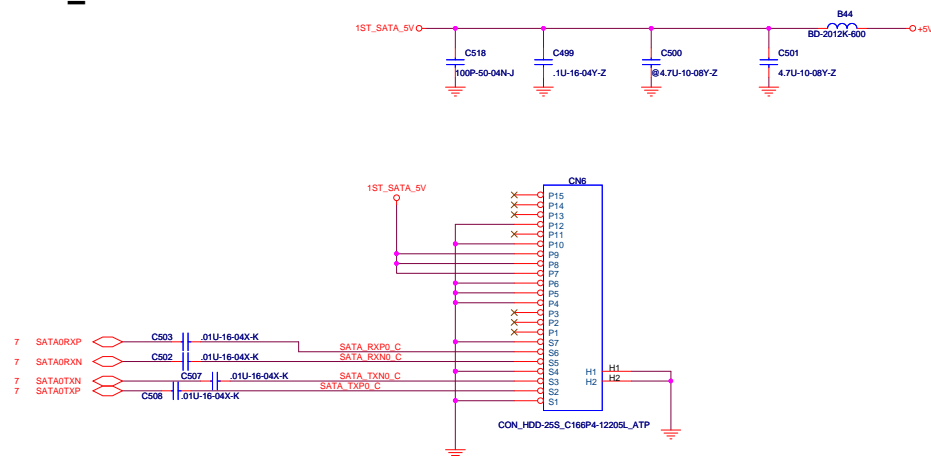
WEBCAM



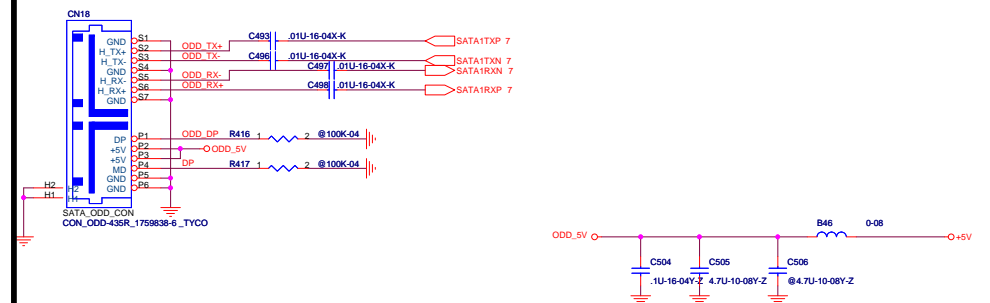
3G_D/B



SATA_HDD



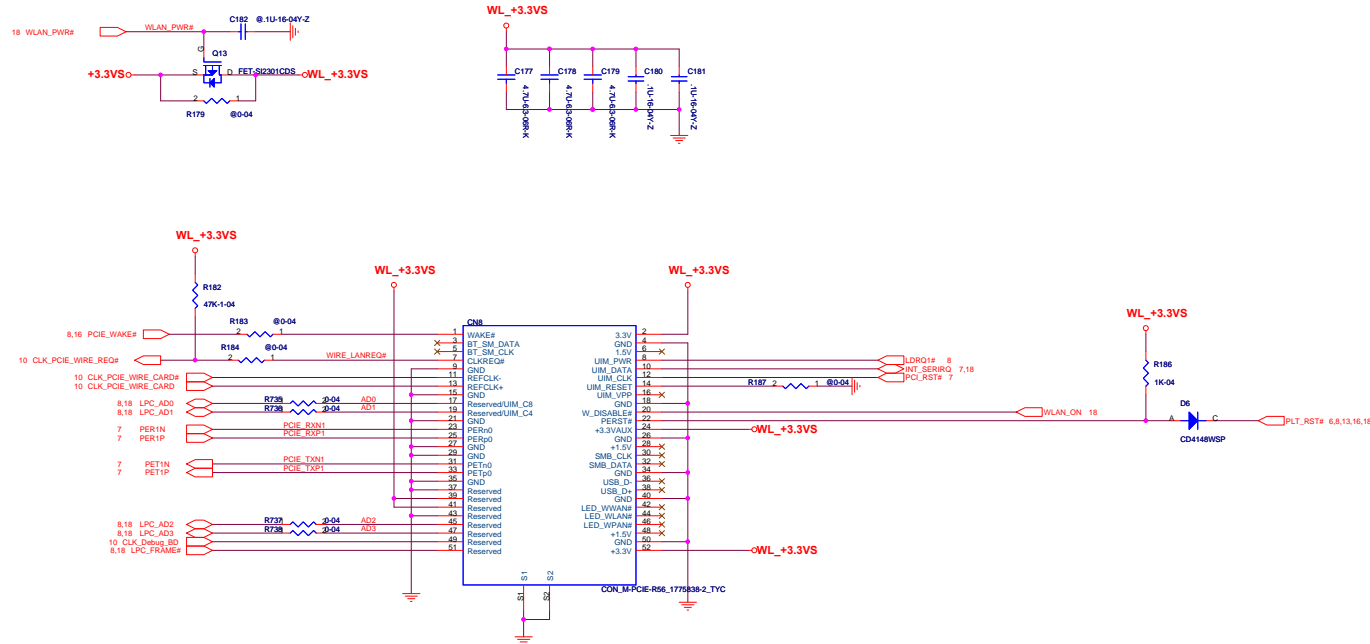
SATA_ODD



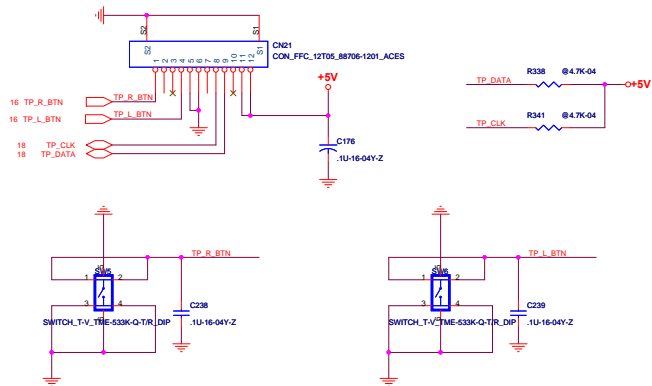
A14IEXX + DDR3

13 USB / WEBCAM / SATA / BT			
File			
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5025			
Date: Thursday, April 22, 2010	Sheet	13	of 26

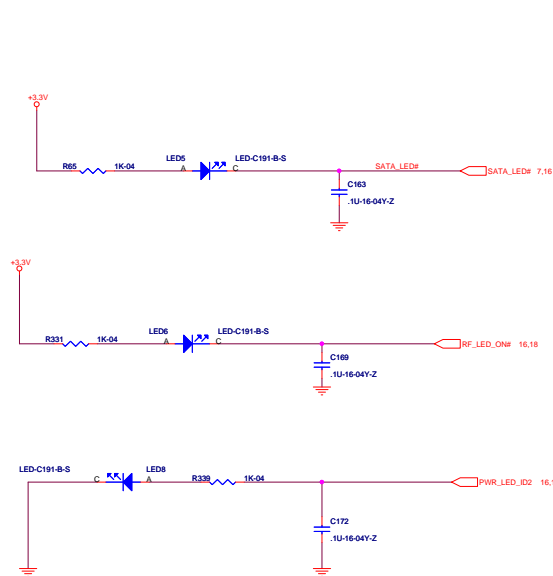
MINI CARD Socket



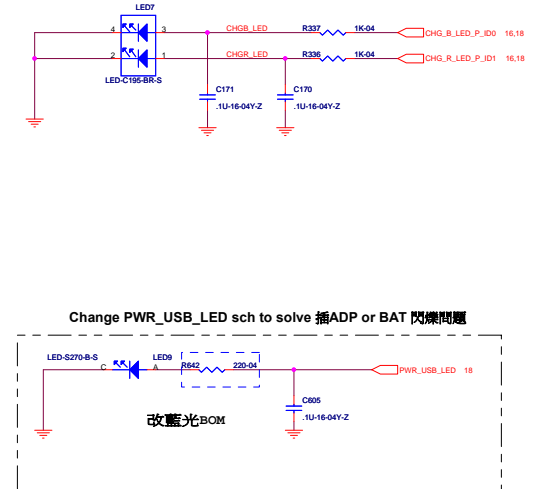
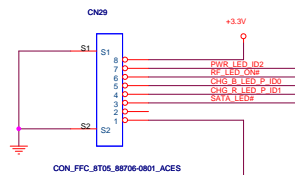
Touch Pad Button



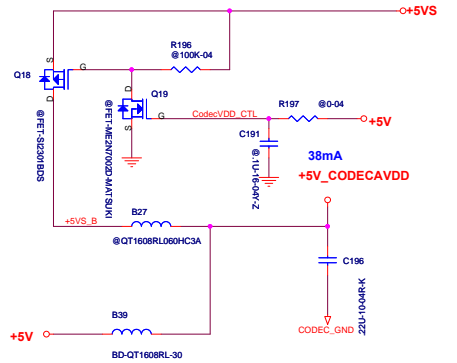
LED



LED BD



AMP VDD



+5V_CODECAVDD

+5V_AMP

CODEC_GND

CODEC_GND

+5V_CODECAVDD

CODEC_GND

+5V_AMP

CODEC_GND

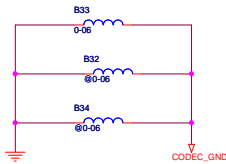
18 AMP_MUTE#

+3.3V

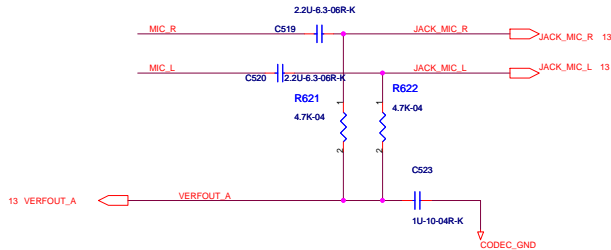
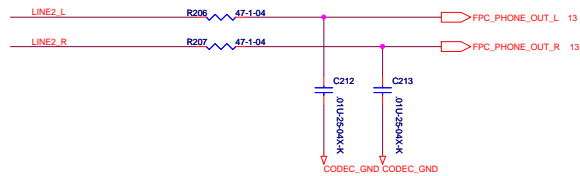
close IC pin

USE 80 MILLS WIDE TRACE
LOCATE UNDER CODEC.

BRIDGING AGND AND DGND PLANES



HEADPHONE



92HD81-YD

CODEC_GND

+5V_AMP

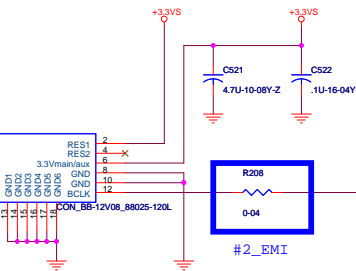
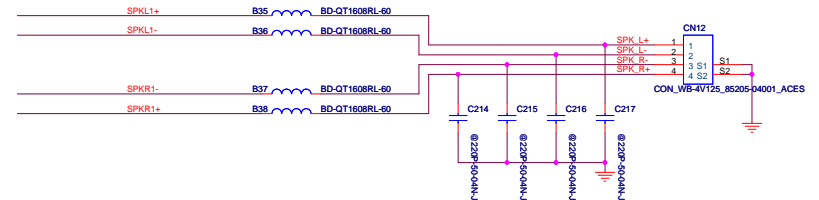
CODEC_GND

+3.3V

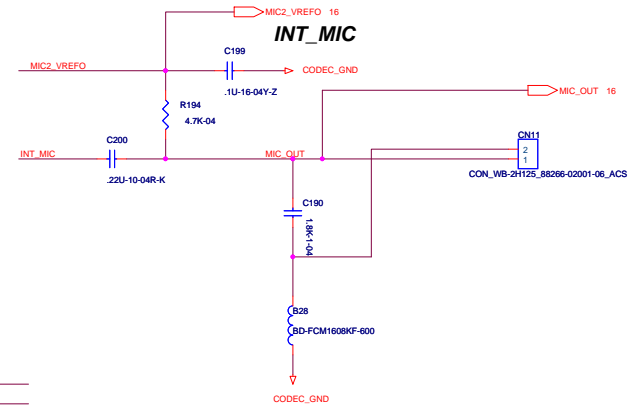
close IC pin

8 ACZ_SDATAOUT

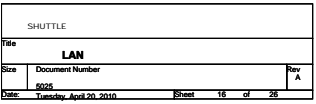
SPEAKER



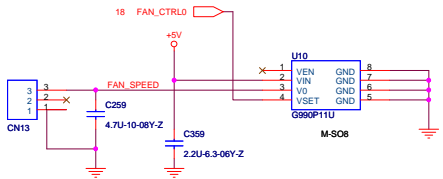
INT_MIC



SHUTTLE		
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Size	Document Number	Rev
5025		A
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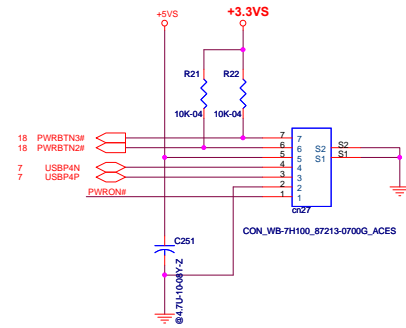


FAN



CON_WB-3H125_85204-03001_ACES

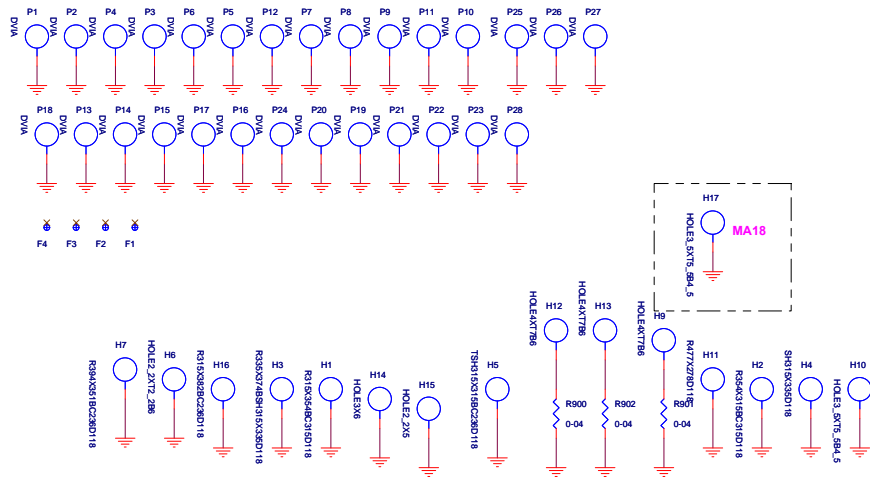
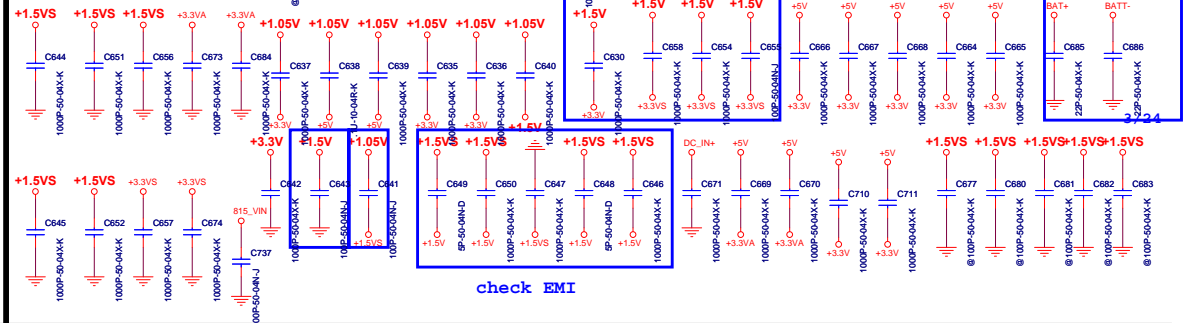
USB SWITCH



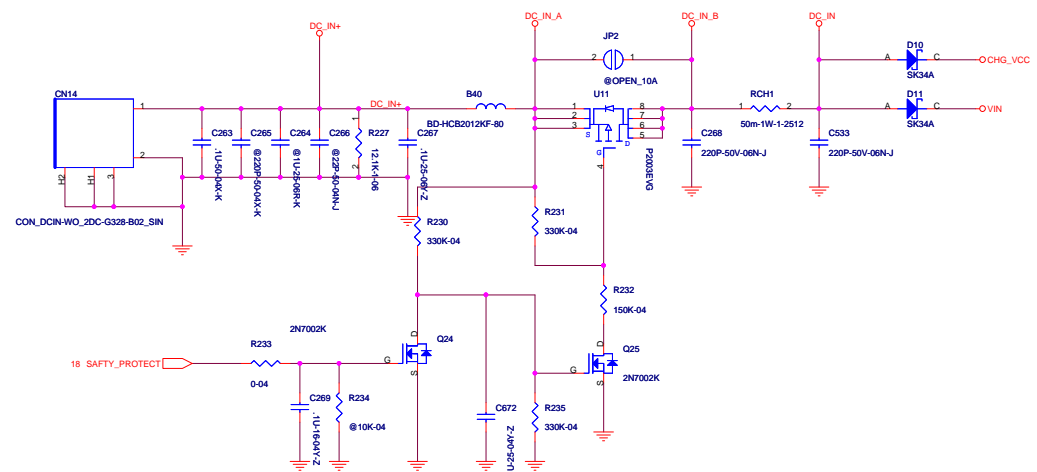
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AC-CAP

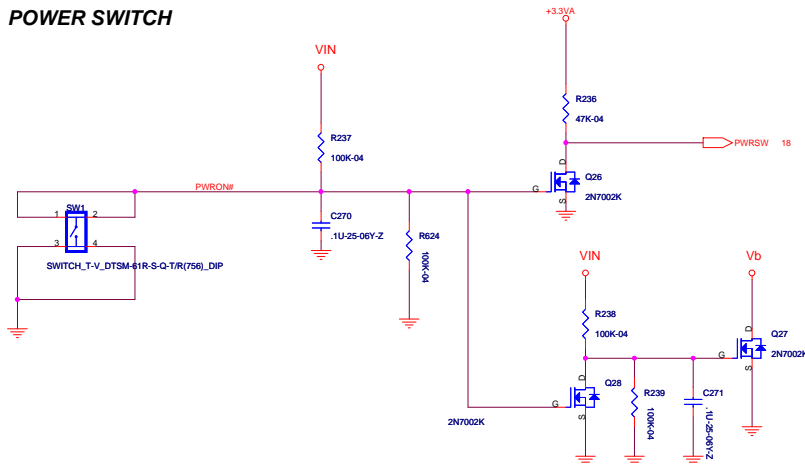
CHECK EMI #5_EMI



DC-IN

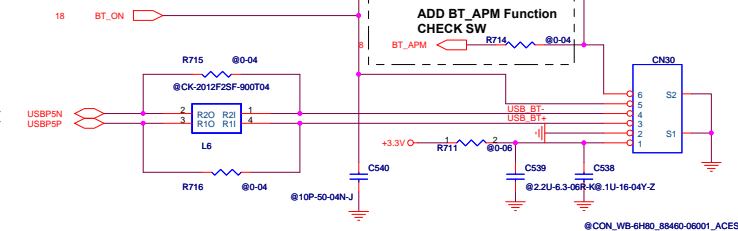


POWER SWITCH



BT CONN

MA13

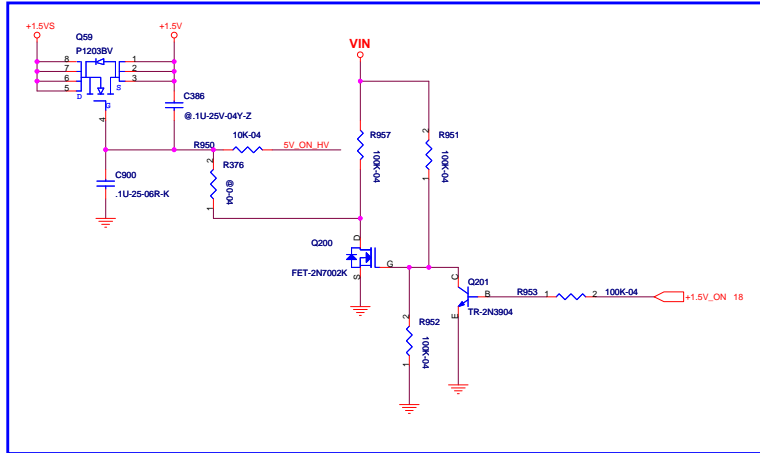
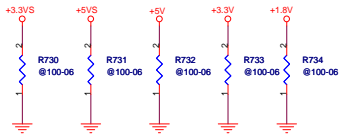
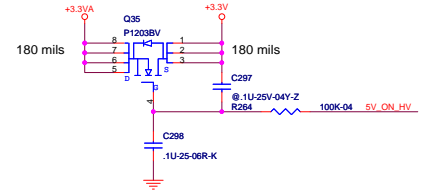
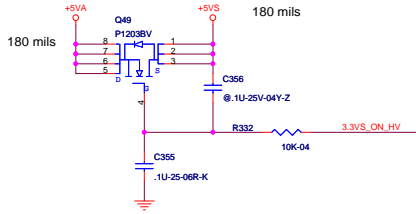
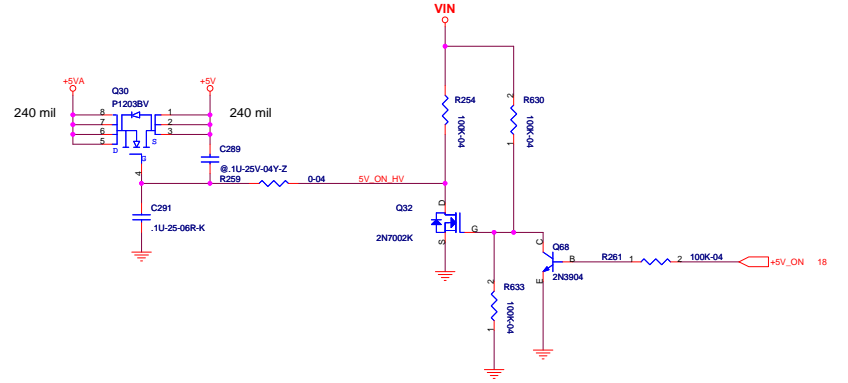
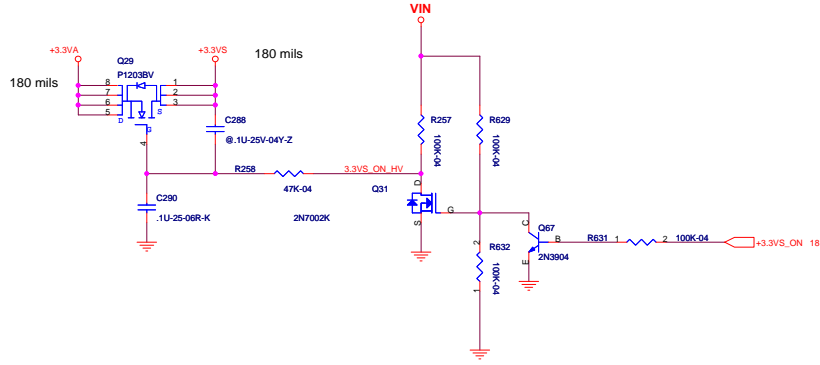


modify 9/28

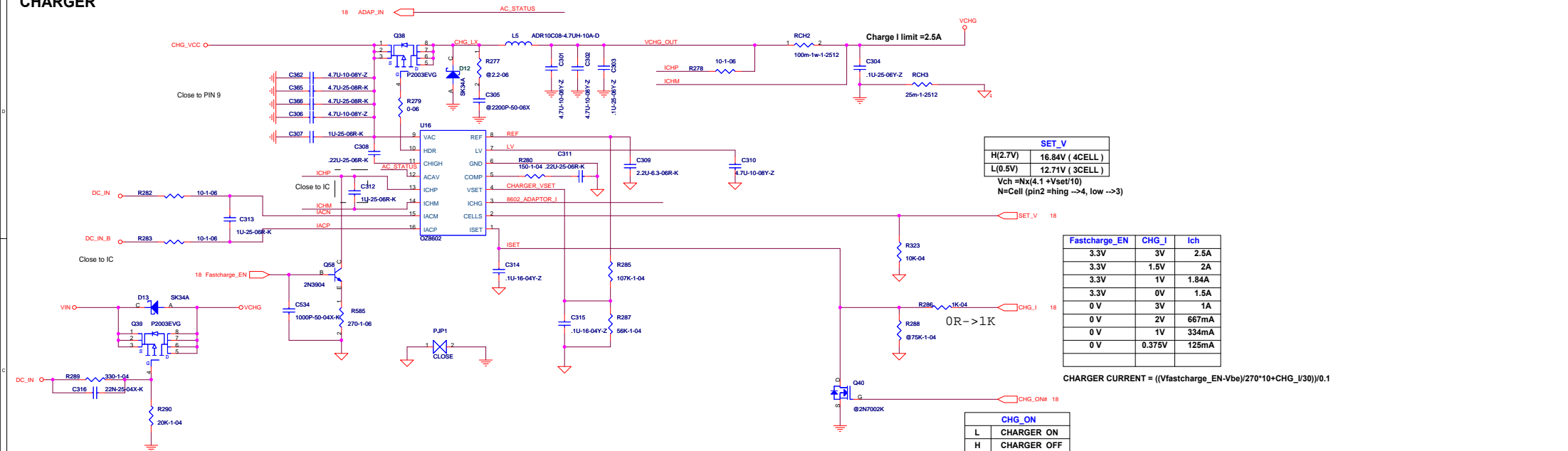
CHECK BT TYPE AND Life power function

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Size	Document Number	Rev	
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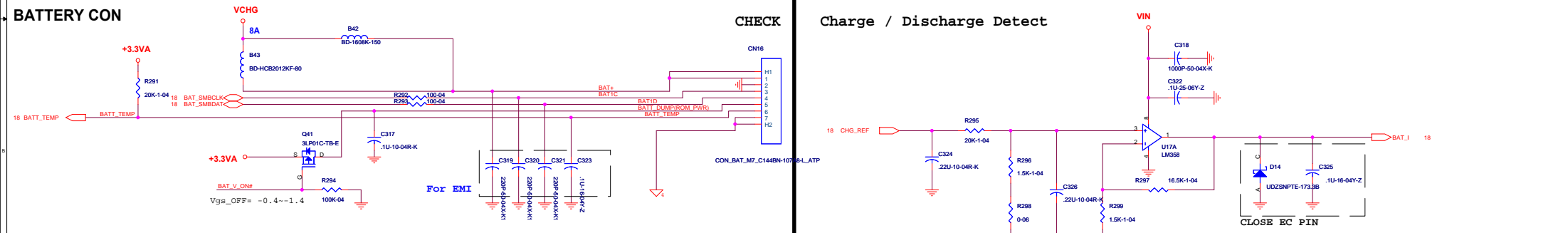
VCC SW



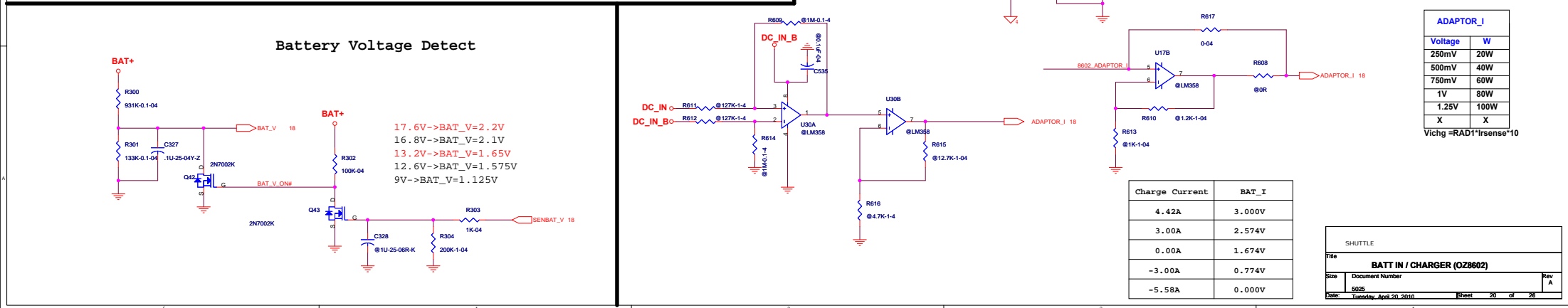
CHARGER

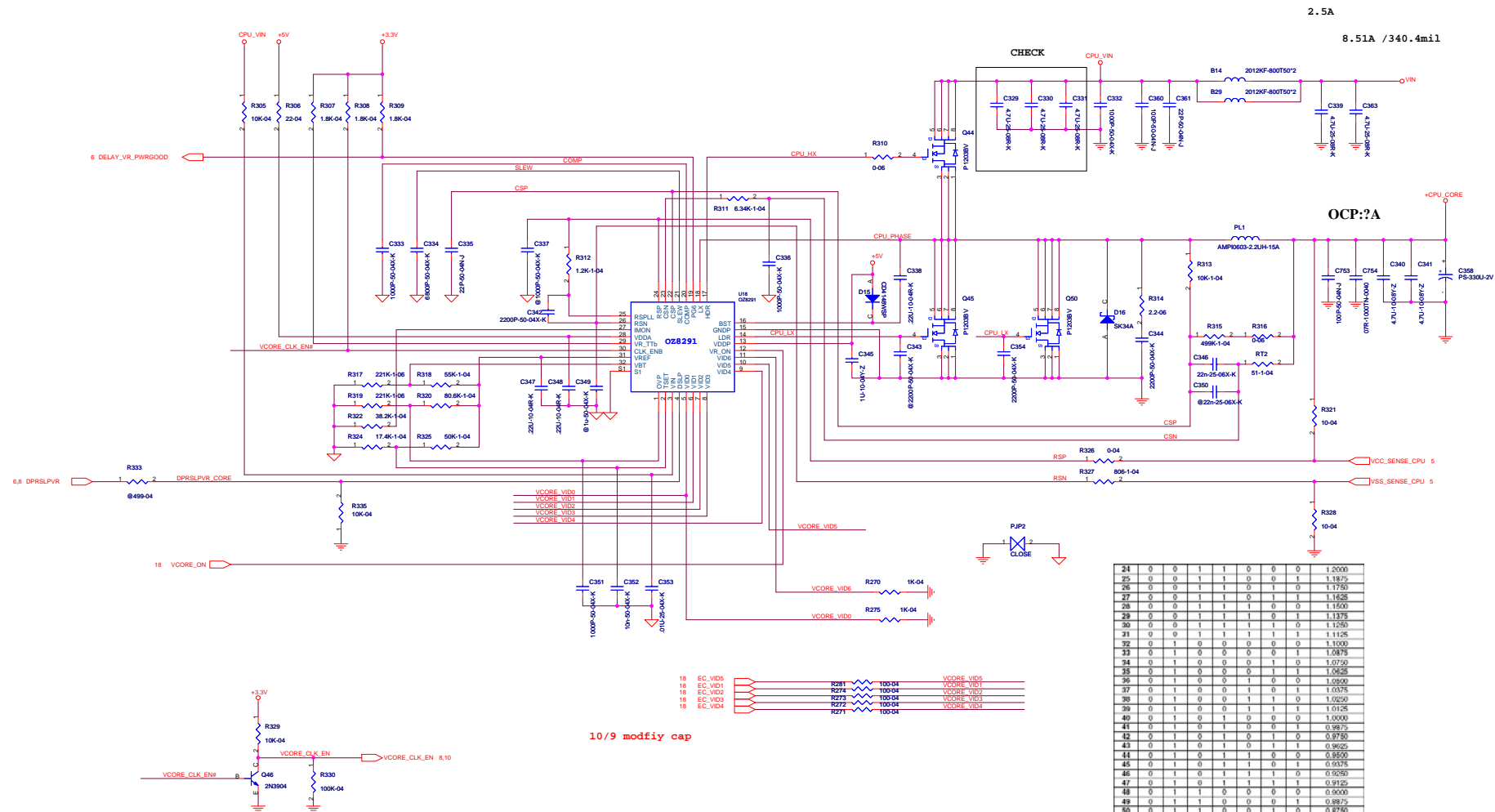


BATTERY CON



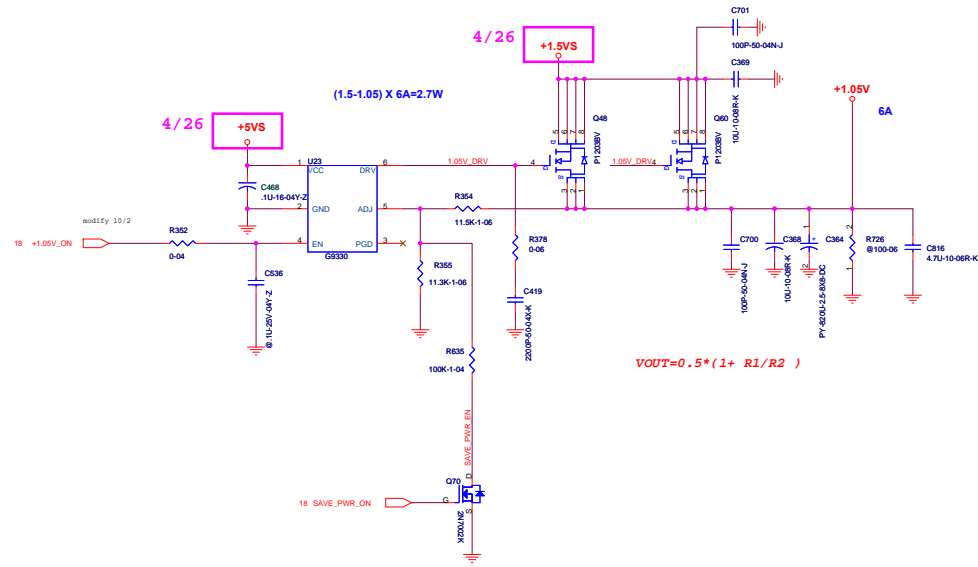
Battery Voltage Detect



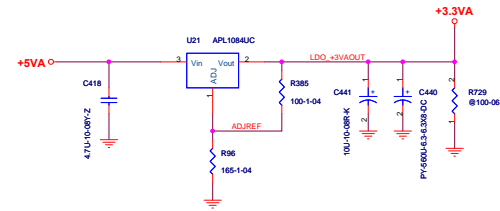
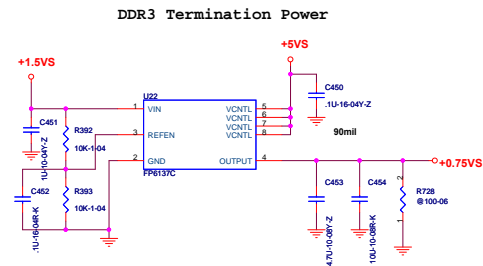
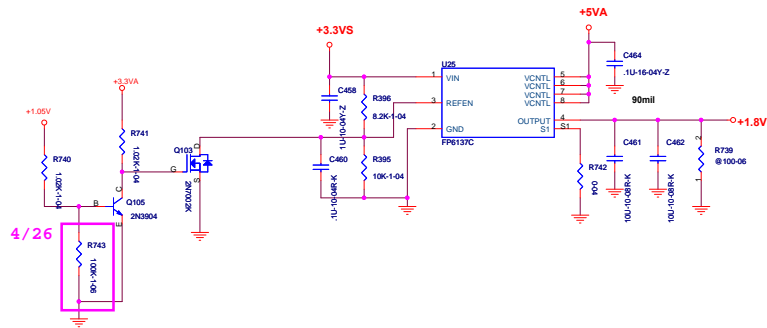


24	0	0	1	1	0	0	1.2000
25	0	0	1	1	0	0	1.1875
26	0	0	1	1	0	0	1.1750
27	0	0	1	1	0	0	1.1625
28	0	0	1	1	0	0	1.1500
29	0	0	1	1	1	0	1.1375
30	0	0	1	1	1	0	1.1250
31	0	0	1	1	1	1	1.1125
32	0	1	0	0	0	0	1.1000
33	0	1	0	0	0	0	1.0875
34	0	1	0	0	0	1	1.0750
35	0	1	0	0	0	1	1.0625
36	0	1	0	0	0	1	1.0500
37	0	1	0	0	0	1	1.0375
38	0	1	0	0	1	1	1.0250
39	0	1	0	0	1	0	1.0125
40	0	1	0	0	1	0	1.0000
41	0	1	0	0	1	0	0.9875
42	0	1	0	0	1	0	0.9750
43	0	1	0	0	1	0	0.9625
44	0	1	0	0	1	0	0.9500
45	0	1	0	0	1	0	0.9375
46	0	1	0	0	1	0	0.9250
47	0	1	0	0	1	1	0.9125
48	0	1	1	0	0	0	0.9000
49	0	1	1	0	0	0	0.8875
50	0	1	1	0	0	1	0.8750
51	0	1	1	0	0	1	0.8625
52	0	1	1	0	0	1	0.8500
53	0	1	1	0	1	0	0.8375
54	0	1	1	0	1	0	0.8250
55	0	1	1	0	1	1	0.8125
56	0	1	1	0	1	0	0.8000
57	0	1	1	0	1	0	0.7875
58	0	1	1	0	1	0	0.7750
59	0	1	1	0	1	0	0.7625
60	0	1	1	0	1	0	0.7500
61	0	1	1	1	1	0	0.7375
62	0	1	1	1	1	0	0.7250
63	0	1	1	1	1	1	0.7125

SHUTTLE		
CPU CORE (OZ8291)		
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Modify (8/17) ADD Life power



SHUTTLE		
File	+1.05V/+1.8V/0.75VS/3.3VA	
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8025		A0
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RB change list:

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T10IL1 Power on Sequence Diagram

